

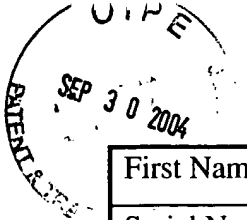


First Named Inventor	Leonard Forbes	GENERAL TRANSMITTAL FORM UNDER 37 CFR 1.8 (LARGE ENTITY)
Serial No.	10/682,590	
Filing Date	October 9, 2003	
Group Art Unit	2818	
Examiner Name	Mai-Huong C. Tran	
Confirmation No.	1986	
Attorney Docket No.	400.257US01	
Title: FULLY DEPLETED SILICON-ON-INSULATOR CMOS LOGIC		

Mail Stop: ISSUE FEE
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Enclosures			
The following documents are enclosed:			
<input checked="" type="checkbox"/>	Form PTOL-85 (Issue Fee Transmittal) in duplicate (2 pgs.);		
<input checked="" type="checkbox"/>	Communication re: Comments on Examiner's Statement of Reasons for Allowance (1 pg.);		
<input checked="" type="checkbox"/>	"Fee Address" Indication Form (1 pg.);		
<input checked="" type="checkbox"/>	A check in the amount of \$1,330.00 (Issue Fee);		
<input checked="" type="checkbox"/>	A check in the amount of \$300.00 (Publication Fee);		
<input checked="" type="checkbox"/>	A check in the amount of \$12.00 (advance order of 4 patent copies);		
<input checked="" type="checkbox"/>	An itemized return-receipt postcard		
Leffert Jay & Polglaze, P.A. P.O. Box 581009 Minneapolis, MN 55458-1009 T - 612/312-2200 F - 612/312-2250			
Please charge any additional fees or credit any overpayments to Deposit Account No. 501373.			
CUSTOMER NO. 27073			
Submitted By			
Name	Kenneth W. Bolvin	Reg. No.	34,125
Telephone	(612) 312-2211		
Signature	<i>Kenneth W. Bolvin</i>		Date
9/27/04			
Certificate of Mailing			
I certify that this correspondence and the identified documents listed on this transmittal are being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop: ISSUE FEE, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450 on September 27, 2004.			
Name	Rhonda L. Foley	Signature	<i>Rhonda L. Foley</i>

(LARGE ENTITY TRANSMITTAL UNDER 37 CFR § 1.8)



First Named Inventor	Leonard Forbes	COMMENTS ON EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE
Serial No.	10/682,590	
Filing Date	October 9, 2003	
Group Art Unit	2818	
Examiner Name	Mai-Huong C. Tran	
Allowed	August 26, 2004	
Confirmation No.	1986	
Attorney Docket No.	400.257US01	
Title: FULLY DEPLETED SILICON-ON-INSULATOR CMOS LOGIC		

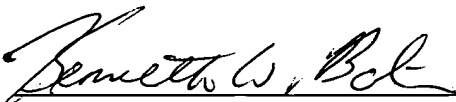
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Applicant notes that the allowed claims define over the prior art for many different reasons. The stated reasons in the Reasons for Allowance should be interpreted as highlighting only one of the many differences between prior art and the claimed invention. Further, certain claimed limitations not discussed in the statement, particularly in dependent claims, provide additional reasons for patentability.

If the Examiner has any questions or concerns regarding this application, please contact the undersigned at (612) 312-2211.

Respectfully submitted,

Date: 9/27/04


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